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REISSUE APPLICATION FOR UNITED STATES PATENT

FOR

BLENDING TEXT AND GRAPHICS FOR DISPLAY ON TELEVISIONS

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BLENDING TEXT AND GRAPHICS FOR DISPLAY ON TELEVISIONS

BACKGROUND

The present invention relates generally to techniques for displaying computer information on televisions.

In a number of applications, users may wish to display computer information on a conventional television receiver. 10 Using a television receiver (which the user may already have) instead of a computer monitor may result in cost savings. In addition, some users may wish to have a computer system which doubles as a television receiver. Thus, the user may watch conventional television programs using a tuner card in the computer system to tune to the desired programs. In devices known as set top computers, a computer system may be mounted atop a conventional television to implement combined television receiving and conventional computer functions. For example, such systems 20 advantageously implement electronic programming guides for computer control of television program access.

Interlacing contributes to the inferior quality of images developed by computer systems that are displayed on television monitors. Interlacing is a technique by which a 25 complete television picture is developed in two passes from top to bottom on the television screen. With interlacing, the first pass paints all the "odd" lines and the second pass paints the "even" lines.

Flicker may occur when the images in the odd lines are 30 very different from the images in the even lines. As the odd and even lines are alternately displayed, the eye may perceive the quick appearing and disappearing of visual information. Flicker is especially noticeable when viewing thin horizontal lines that only take up a single odd or even row. 35 The same objectionable phenomenon occurs at the horizontal boundaries of text.

A flicker filter may be used to reduce such flickering. A flicker filter is generally a 3 or 5 tap finite impulse response (FIR) filter. An N-tap filter uses N multiplications and N-1 additions per color component of each output pixel. An equation representing the calculation implemented by a 3-tap filter is as follows:

Pout=T1*P1+T2*P2+T3*P3

T1, T2 and T3 are the tap values and P1, P2 and P3 are the values of adjacent pixels. Thus, flicker filters are quite computationally intensive.

Some flicker filters work by altering the information in the 50 processing than conventional flicker filters. odd and even lines of a television picture so that the alternating lines are more similar to each other. In this way, when the lines appear and disappear in the interlacing process, the flicker is less noticeable. The more similar the lines are made to appear, the less flicker is visible. Other 55 that provides the interface to a bus 22, a multifunction I/O flicker filters reduce flicker by completely discarding every other line of the image, and displaying two successive passes of the remaining lines. Because the interlaced lines are now identical, flicker is less visible. In either case, vertical resolution is sacrificed. The obvious trade-off is that as 60 flicker is reduced, more and more information is altered or lost from the original picture. Thin, horizontal lines may disappear. Small text may become unreadable.

The quality of flicker filters may vary based on design and cost. Low end flicker filters apply their techniques to the 65 entire screen indiscriminately or blindly. These types of flicker filters may cause undesirable results, when only a

small portion of the screen needs filtering, by adversely altering regions that were unnecessarily filtered.

Thus, there is a continuing need for ways to economically improve the display of information from processor-based 5 systems on televisions.

SUMMARY

In accordance with one aspect, a method includes acquiring a graphics primitive to be displayed on a television receiver. The transparency of portions of the primitive are selectively adjusted relative to a background to reduce flicker when the primitive is displayed on a television

Other aspects are set forth in the accompanying detailed 15 description and claims.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of a computer system according to a preferred embodiment;

FIG. 2 is a block diagram of a software display driver architecture according to the preferred embodiment;

FIG. 3 is a pixel block diagram of a character rendered according to the preferred embodiment; and

FIG. 4 is a flow chart for software for implementing one embodiment of the present invention.

DETAILED DESCRIPTION

Referring to FIG. 1, a processor-based system 10 may use a television receiver 18 to display information. Although the system 10 is illustrated as a personal computer, the present invention may also be implemented on other computer platforms or architectures, such as set-top boxes, processorbased appliances, video game players, and digital video disk ("DVD") players.

A processor 11 is coupled to a host bridge hub 12, such as an Intel® 82810 graphics and memory controller hub, in one embodiment of the invention. The host bridge hub 12 may include a memory controller for accessing a main memory 14 (e.g. synchronous dynamic random access memory ("SDRAM")), and graphics controller 13 with motion compensation video capabilities. The graphics controller 13 may also include a digital video output port which couples (via an external video encoder 16) to the analog television receiver ₄₅ 18.

As will be explained below, font and graphics libraries may be used in one embodiment of the invention for rendering fonts and lines in a manner that flicker is reduced. In some embodiments flicker may be reduced with less

The host bridge hub 12 may also couple an input/output ("I/O") controller hub 20, such as an Intel® 82801 I/O hub in one embodiment of the invention. The I/O controller hub 20 may be an integrated multifunctional I/O controller hub controller 24, and a firmware hub 26 and may integrate other functions, such as an enhanced direct memory access ("DMA") controller, an interrupt controller, a timer, an integrated drive electronics ("IDE") controller for providing IDE ports, and a universal serial bus ("USB") host controller, as examples. Four IDE devices, such as a hard disk drive 28, a compact disk read only memory ("CD-ROM") drive 30, and a DVD drive 32 may be coupled to the hub 20. A television tuner/capture card 49 may also be coupled to the hub 20.

The I/O controller hub 20 may support two codecs such as the audio codec 36 and modem codec 38. The audio codec 36 may be coupled to a speaker 40 and the modem codec 38 may be coupled to a network 41, such as a telecommunications, satellite or cable network.

The multifunction I/O controller 24 typically includes a number of functions, such as a floppy disk drive controller for coupling a floppy disk drive 42, a keyboard controller for connecting to a keyboard 44 and a pointing device 46, a serial communications controller for providing at least one serial port 48, and a parallel port interface for providing at least one parallel port 50. In one embodiment of the 10 invention, the keyboard 44 and pointing device 46 are coupled via an infrared transmitter/receiver 52 for wireless communications.

Referring to FIG. 2, a display driver architecture 70 according to one embodiment of the present invention, may 15 include font library 72 and graphics library 74 for displaying lines and text on the receiver 18. The resulting fonts and graphics are prepared for display on the receiver 18 by the display driver architecture 70.

More specifically, in a display driver architecture 70, in 20 accordance with one embodiment of the invention, a display driver 76 is a dynamic link library ("DLL") that translates device-independent commands from a graphics device interface ("GDI") 78 into commands and actions that the graphics controller 13 may use to draw graphics on the receiver 18. GDI commands may include as examples, bitmap operations, managing graphics objects, managing color palettes, and drawing lines, curves, filled shapes, and text. The device independent commands originate from a software application, such as a drawing program, slide show program, movie program, game program or other applica- 30 tion. The display driver 76 may also supply information to the operating system regarding the display hardware (e.g. receiver 18), such as color resolution, graphics capabilities, screen size and resolution, and other display features. The fonts and graphics are rendered into a frame buffer 80.

To take advantage of special features of the graphics controller 13, a display minidriver may be provided in one embodiment of the invention. To take advantage of the font and graphics libraries, the display driver 76 may contain program code to render font and graphics elements according to special alpha blending values embedded with font and graphic elements. The graphics elements may include text, characters, lines, shapes and other graphics primitives. Alpha blending uses pre-defined values, called alpha values, to control the way graphics information is displayed, such as the level of transparency or opacity of graphics primitives. Thus, pixel colors may be blended in varying proportion with the color of the primitive being drawn. The proportion is the transparency or alpha value.

Instead of using the alpha values to create visual effects, the alpha values may be assigned to overcome flickering. That is, by smoothing the edges of horizontal features, the amount of flickering which results may be reduced. Thus, by assigning alpha values which cause background colors to blend with graphics primitives along horizontal lines, the flickering which results from display on analog television receivers may be decreased without the need for computationally complex FIR filtering.

By focusing on the horizontal components which cause filtering, vertical components of graphics primitives need not be affected. Thus, compared to other filtering techniques which are applied indiscriminately to all of the features on the display, the use of alpha blending techniques may be adapted, in some embodiments of the invention, to only adjust the display of horizontal features.

Referring now to FIG. 3, a letter "T" is prepared according to one embodiment of the present invention. A bitmap may be produced by the display driver 76 according to the

font and graphics libraries 72 and 74 and input signals from an application. A normally rendered "T" is shown in solid black in FIG. 3. The enhancements shown around the "T" are the result of an "alpha blending" method according to alpha values embedded with font and graphic primitives.

A font and graphics primitive may have at least one associated alpha value. The alpha value instructs the display driver 76 how to smooth (i.e. how much transparency to implement for) the graphics primitive to reduce flicker. Progressive smoothing may be applied in one or many more stages. For example, the letter "T" may have two alpha values applied in one embodiment of the invention. A first value (0.5) in a first stage is applied to pixels adjacent (once removed) the horizontal sections of the "T" and a second value (0.25) is applied in a second stage to pixels twice removed from the horizontal sections.

The alpha (α) blended pixels may take into account the color of the background ($P_{overlay}$) and foreground (font or graphics) (P_{in}) to smooth out horizontal primitive edges between the overlay or background and the primitive:

$$P_{out} = \alpha P_{overlay} + (1-\alpha)P_{in}$$

or this may reduce to

$$P_{out} = \alpha (P_{overlay} - P_{in}) + P_{in}$$

Referring next to FIG. 4, software 82 for implementing the alpha blending in the graphics controller 13 may be stored on the hard drive 28, in accordance with an embodiment of the present invention. The flow begins by determining whether a primitive has been requested for display as indicated in diamond 84. If so, the primitive may be acquired from the libraries 72 or 74 as the case may be, as indicated in block 86. The color value for the background is then acquired as indicated in block 88. Similarly, the color value for the primitive is acquired as indicated in block 90. The alpha blending equation, set forth above, is then used to calculate a new color value or grey scale value, as indicated in block 92.

The pixels of a video display include three color components such as red, green and blue in an RGB tricolor space. The color values of the three color components are added together to produce a displayed color having a color value.

Using the hypothetical example illustrated in FIG. 3, the color values for pixels $P_{2,9}$ and $P_{2,10}$ can be calculated. Pixel $P_{2,9}$ has a alpha value of 0.5 applied and pixel $P_{2,10}$ has an alpha value of 0.25. These alpha values may be associated with the primitive "T" in the font library. For example, the primitive and its alpha values may be stored together as a 61s.

Assuming a black letter (P_{in} =1 in grey scale) on a white background ($P_{overlay}$ =0 in grey scale), $P_{2,9}$ =0.5*0+0.5*1. Broken down into its color components P_{out} yields:

 P_{RED} =0.5*0h+0.5*FFh=7Fh

 P_{GREEN} =0.5*0h+0.5*FFh=7Fh

 P_{BLUE} =0.5*0h+0.5*FFh=7Fh

Taken together the color components yield a relatively dark grey scale pixel next to the black pixel $P_{2,8}$.

For pixel P_{2,10} the color components are:

 P_{RED} =0.25*0h+0.75*FFh=BDh

 P_{GREEN} =0.25*0h+0.75*FFh=BDh

 $P_{BLUE}=0.25*0h+0.75*FFh=BDh$

Taken together the color components yield a lighter grey scale pixel.